

Figure 1

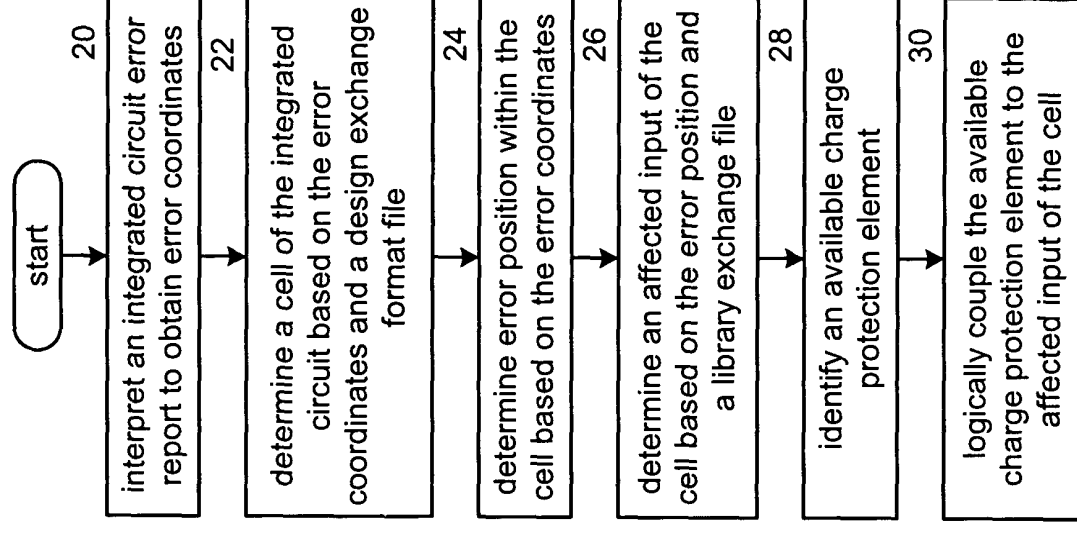


Figure 2

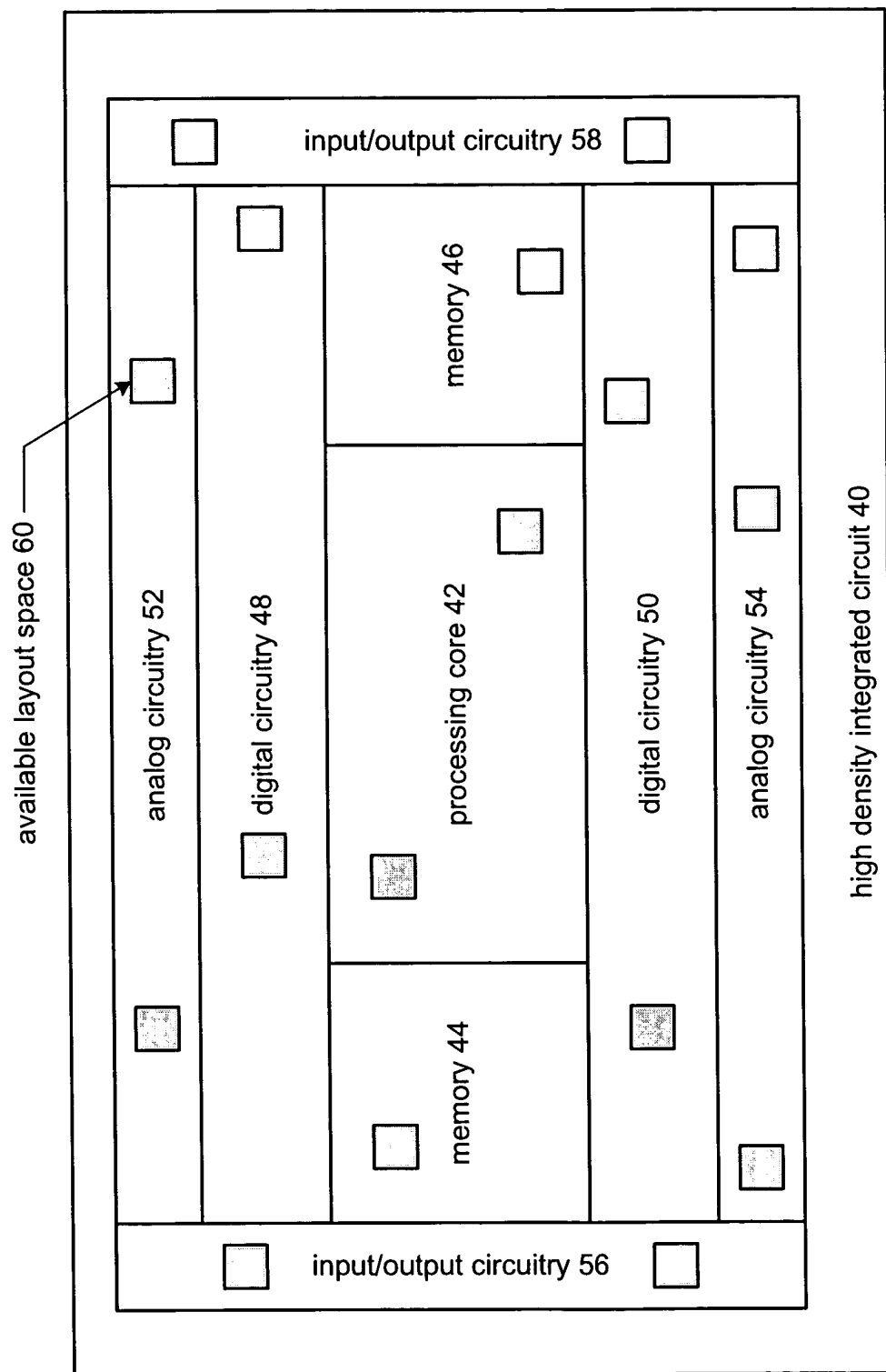


Figure 3
initial IC layout

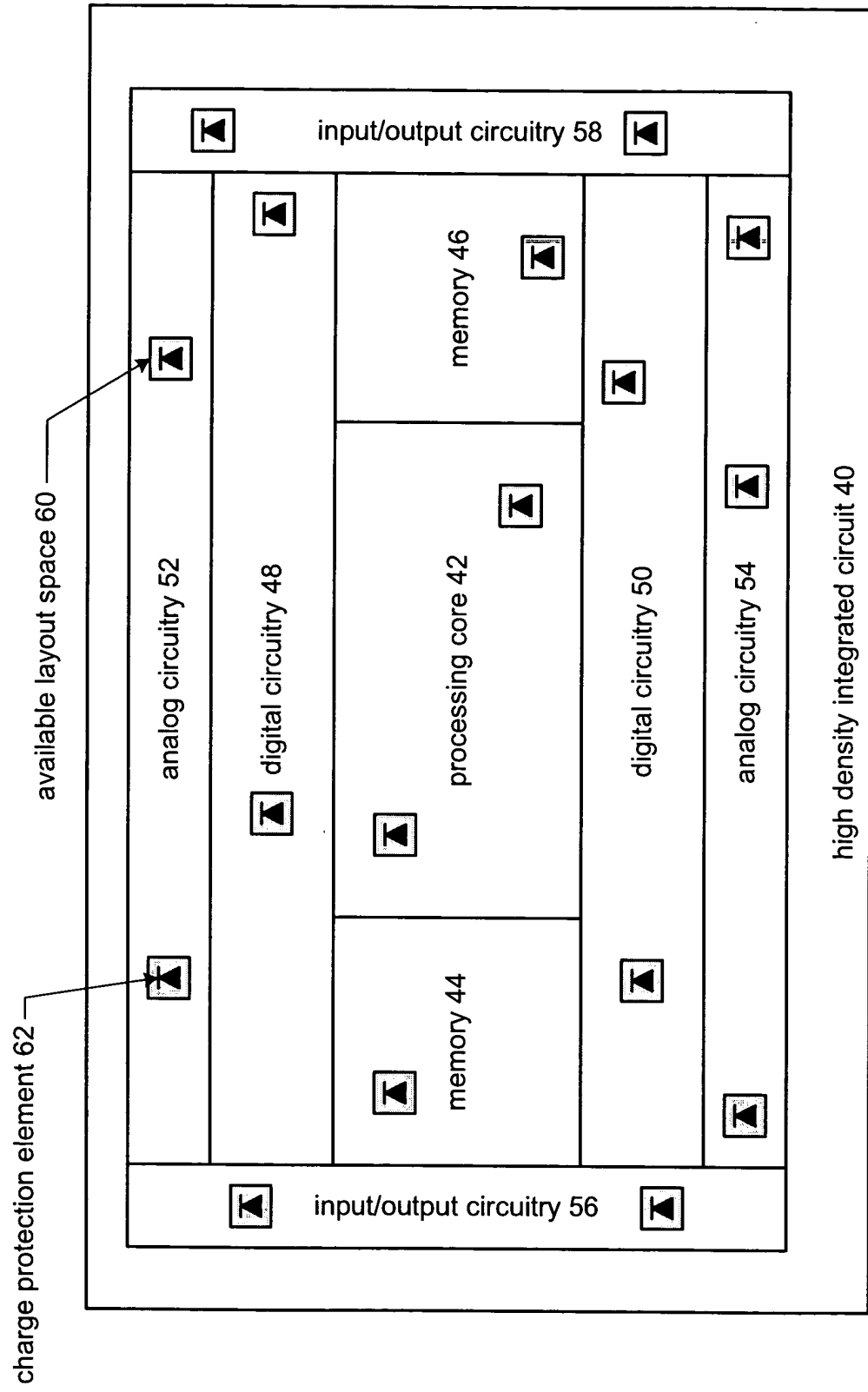


Figure 4
intervening IC layout

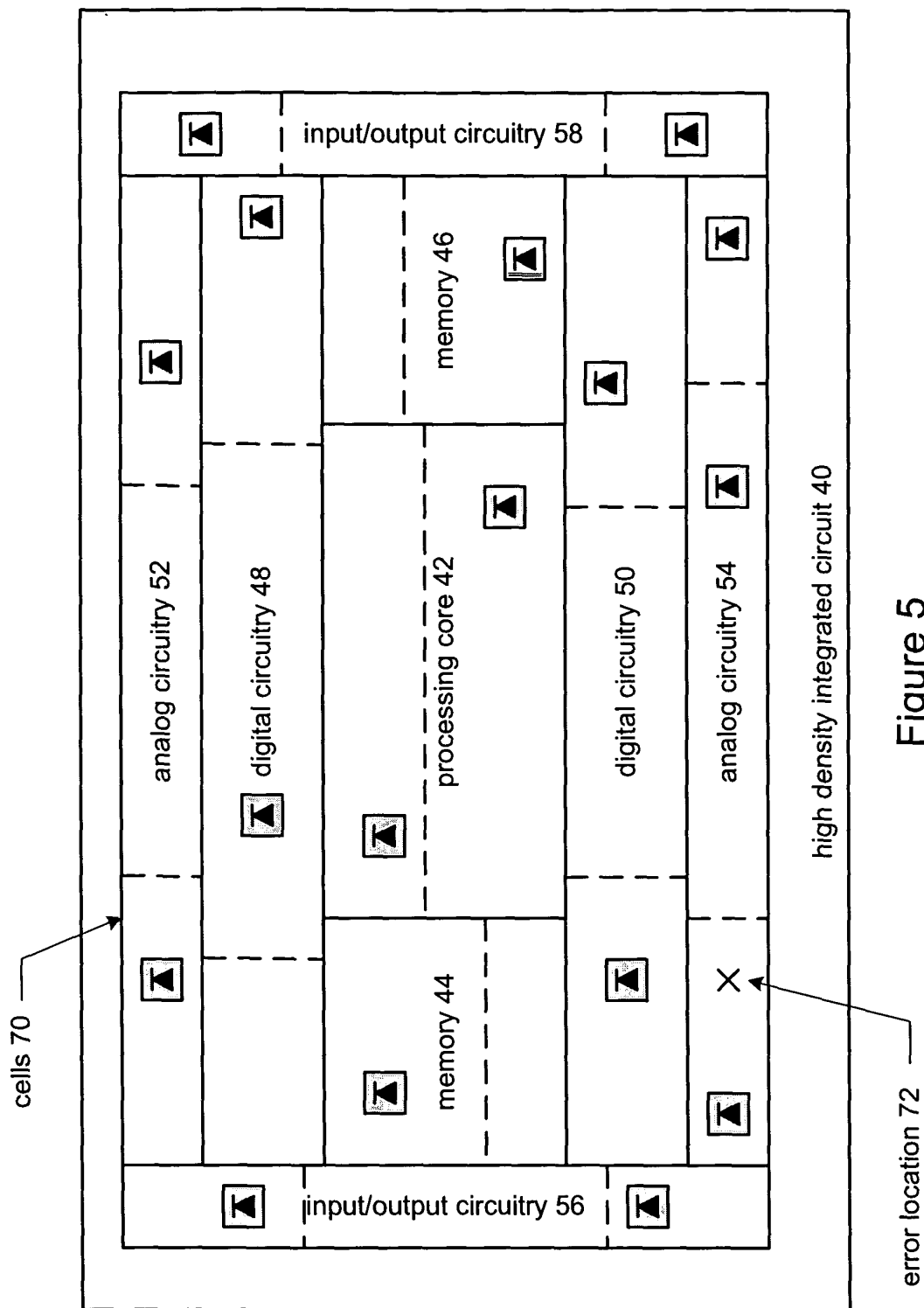


Figure 5
identify antenna violation

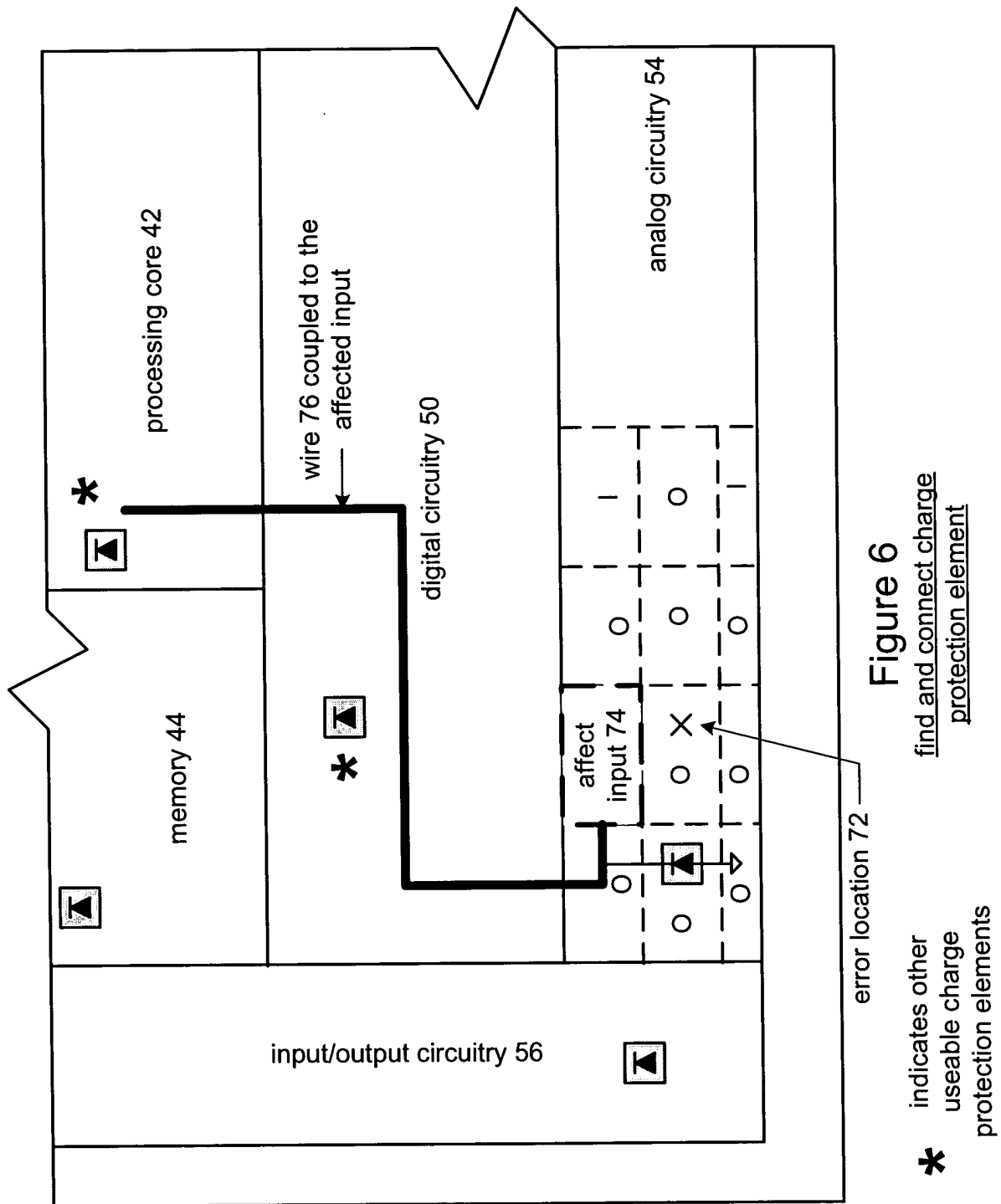


Figure 6